

Layer 0 Silicon Tracker Milestones

Fiscal Year

ID	Milestone	2004				2005				2006			
		Q1	Q2	Q3	Q4	Q1	Q2	Q3	Q4	Q1	Q2	Q3	Q4
3	Probe Stations Ready	11/3											
18	SASEQ Test Stands Available	11/3											
142	Module Burn-in Stands Available	11/3											
112	Freeze Mechanical Parameters		12/16										
17	SVX4 Chips Available		1/5										
121	Support Structures Ready For Production		2/20										
9	Release Sensors for Production		3/19										
24	Release Hybrids for Production		3/26										
45	Release Analog Cables for Production		3/26										
69	Release Twisted-pair Cables for Production		3/26										
59	Release Digital Cables for Production		4/9										
78	Release Junction Cards for Production		4/23										
28	Hybrid Boxes Available			7/6									
81	All Junction Cards Produced and Tested			7/20									
97	Release HV System for Production			8/10									
136	All Support Structures Completed			8/17									
63	All Digital Cables Delivered and Tested			8/24									
138	All Support Structures Received At Fermilab			8/24									
48	All Analog Cables Delivered and Tested			9/29									
73	All Twisted-pair Cables Assembled and Tested			10/13									
91	Release Adapter Cards for Production			11/10									
15	All Sensors Delivered and Tested			11/17									
156	Silicon L0 Module Production Begun			1/18									
39	All L0 Hybrids Delivered, Stuffed, and Tested			1/19									
94	All Adapter Cards Delivered and Tested			2/23									
163	Silicon L0 Module Production Complete			3/24									
168	Silicon L0 Module Production and Testing Complete			5/16									
107	Full Chain System Test Completed			6/9									
189	Layer 0 Silicon Detector Assembly and Testing Complete											7/14	
191	Layer 0 Silicon Detector Ready to Move to DAB											7/21	